## 7001

## Switch/Control Mainframe 80-channel



The Model 7001 is a half-rack, high density, two-slot mainframe that supports the widest range of signals in the test and measurement industry. DC switching capabilities from nanovolts to 1100 V and femtoamps to 5 A , as well as RF and optical switch support, make the Model 7001 a versatile production test tool for a wide array of applications.

Built-in scan control eliminates the need for the computer to control every step of the test procedure. Simply program the 7001 to control channel spacing, scan spacing, and the number of scans. A built-in non-volatile memory stores up to 100 complete switch patterns. You can include these memory locations as part of the scan list.

Up to 80 channels of $\mathbf{2}$-pole switching. Each slot of the 7001 can accommodate up to 40 channels. This means fewer switch cards are

- DC, RF, and optical switch capability
- Supports industry's broadest range of signals
- Integrates easily with DMM and SourceMeter ${ }^{\circledR}$ instruments
- Full channel status display
- 2 card slots
- Supports more than 30 switch/ control cards


## Ordering Information

$7001 \quad \begin{aligned} & \text { 80-channel Switch/ } \\ & \text { Control Mainframe }\end{aligned}$ required, reducing the amount of switching hardware needed. Higher density also provides extra capacity and flexibility.
Analog backplane. The 7001's analog backplane is used by the high density switch cards. The backplane eliminates intercard wiring and increases configuration flexibility. Two cards can be connected through the backplane to create a $1 \times 80$ multiplexer, a $4 \times 20$ matrix, or a multiplexer/matrix combination that provides matrix row expansion.

Channel status display. See the status of every channel simultaneously. The vacuum fluorescent display of the 7001 shows the open/close status of each channel in the mainframe simultaneously. The graphical display pattern makes it much easier to configure a test system, make modifications, or debug an existing program. The status of the cards in both slots is displayed side by side on the same screen.

Easy to set up and use. The 7001 has a number of built-in features that make it easy to set up, run, change, or modify. It conforms to IEEE-488.2 and SCPI (Standard Commands for Programmable Instruments). All aspects of the instrument can be programmed from the front panel and over the IEEE bus.

Trigger Link. Trigger Link is a high speed trigger bus that provides simple trigger coordination between the Model 7001 and other instruments. This bus eliminates GPIB communication delays during scanning to increase overall system throughput dramatically.

More than 30 cards available. The 7001 switch cards accommodate a broad range of signals, maintain very high accuracy, and will not degrade signal quality. By minimizing signal errors, these cards will prevent degradation due to offset voltage, isolation resistance, and leakage current.
With its broad range of available cards, the 7001 provides multi-pole switching. Cards such as the 7011 can be used in either 2 - or 4 -pole configuration. If a card does not have the pole capacity required, the 7001 can still accommodate the application-just select the CARD PAIR function. It allows the channel closures in both slots to be synchronized for up to 8 -pole switching.

## ACCESSORIES AVAILABLE

COMMUNICATION INTERFACES
and CABLES
7007-1 Double Shielded, Premium GPIB Cable, 1 m
7007-2 Double Shielded, Premium GPIB Cable, 2 m
KPCI-488LPA IEEE-488 Interface/Controller for the PCI Bus
KUSB-488A IEEE-488 USB-toGPIB Interface Adapter

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## System

CAPACITY: 2 plug-in cards per mainframe
MEMORY: Battery backed-up storage for 100 switch patterns
SWITCH SETTLING TIME: Automatically selected by the mainframe for each card. Additional time from 0 to 99999.999 seconds can be added in 1 ms increments.
TRIGGER SOURCES:
External Trigger (TTL-compatible, programmable edge, 600 ns minimum pulse, rear panel BNC ).
IEEE-488 bus (GET, *TRG)
Trigger Link
Manual (front panel)
Internal Timer, programmable from 1ms to 99999.999 seconds in 1 ms increments.
STATUS OUTPUT: Channel Ready (TTL-compatible signal, rear panel BNC). Low going pulse ( $10 \mu \mathrm{~s}$ typical) issued after relay settling time. For two different switch cards, 7001 will be set to the slowest relay settling time.
SWITCHING SEQUENCE: Automatic break-before-make. MAINFRAME DIGITAL I/O: 4 open-collector outputs ( 30 V maximum pull-up voltage, 100 mA maximum sink current, $10 \Omega$ output impedance), 1 TTL compatible input, 1 common.
RELAY DRIVE: 700 mA maximum for both card slots.
CARD SIZE: 32 mm high $\times 114 \mathrm{~mm}$ wide $\times 272 \mathrm{~mm}$ long ( $1^{1 ⁄ 4} \mathrm{in}$ $\times 4^{1 / 2}$ in $\times 10^{3 / 4} \mathrm{in}$ ).
CARD COMPATIBILITY: Fully compatible with all 7XXX cards.

## Throughput

EXECUTION SPEED OF SCAN LISTT:

|  | 7011 Card | 7015 Card |
| :--- | :---: | :---: |
| Individual Channels: | 130/second | $500 /$ second |
| Memory Setups: | 125/second | 450/second |

TRIGGER EXECUTION TIME (maximum time from activation of Trigger Source to start of switch open or close ${ }^{2}$ ):

| Source | Latency | Jitter |
| :--- | :--- | :--- |
| GET $^{3}$ | $200 \mu \mathrm{~s}$ | $<50 \mu \mathrm{~s}$ |
| *TRG | 5.0 ms |  |
| Trigger Link | 200 | $\mu \mathrm{~s}$ |
| External | 200 | $\mu \mathrm{~s}$ |

## NOTES

1. Rates include switch settling time of cards: 3 ms for 7011 and $500 \mu \mathrm{~s}$ for 7015 cards.
2. Excluding switch settling time.
3. Assuming no IEEE-488 commands are pending execution.

## IEEE-488 Command

 Execution Time|  | Execution Time ${ }^{\mathbf{1}}$ |  |
| :---: | :---: | :---: |
| Command | Display Off | Display On |
| OPEN (@1!1) | 7.5 ms | 8.5 ms |
| CLOS (@111) | 7.5 ms | 8.5 ms |
| MEM:REC M1 | 5.0 ms | 6.0 ms |

## NOTES

1. Measured from the time at which the command terminator is taken from the bus to the time at which the relay begins to open or close.

## Analog Backplane

SIGNALS: Four 3-pole rows (Hi, Lo, Guard). These signals provide matrix and multiplexer expansion between cards within one mainframe.
MAXIMUM VOLTAGE: 250 V DC, $250 \mathrm{~V} \mathrm{rms}, 350 \mathrm{~V}$ AC peak, sig. nal path to signal path or signal path to chassis.
MAXIMUM CURRENT: 1A peak.
PATH ISOLATION:
$>10^{10} \mathrm{~S},<50 \mathrm{pF}$ path to path (any Hi, Lo, Guard to another Hi, Lo, Guard).
$>10^{10} \Omega,<50 \mathrm{pF}$ differential (Hi to Lo or Hi, Lo to Guard). $>10^{\circ} \Omega,<75 \mathrm{pF}$ path to chassis.
CHANNEL CROSSTALK: <-65dB @ 1 MHz ( $50 \Omega$ load).
BANDWIDTH: $<3 \mathrm{~dB}$ loss at 100 MHz ( $50 \Omega$ load).

## IEEE-488 BUS IMPLEMENTATION

STANDARDS CONFORMANCE: Conforms to SCPI-1990, IEEE-488.2, and
IEEE-488.1.
MULTILINE COMMANDS: DCL, LLO, SDC, GET, GTL, UNT, UNL, SPE, SPD.
UNILINE COMMANDS: IFC, REN, EOI, SRQ, ATN.
Interface functions: SH1, AH1,T5, TE0, L4, LE0, SR1, RL1, PP0, DC1, DT1, C0, E1.

|  | GENERAL |
| :---: | :---: |
|  | DISPLAY: Dual-line vacuum fluorescent. <br> 1st line:20-character alphanumeric. <br> 2nd line:32-character alphanumeric. <br> REAR PANEL CONNECTORS: <br> IEEE-488 <br> 8-pin micro-DIN connector for digital I/O <br> 8 -pin micro-DIN for Trigger Link <br> 8-pin micro-DIN for Trigger Link expansion <br> BNC for External Trigger <br> BNC for Channel Ready <br> POWER: 100 V to $240 \mathrm{Vrms}, 50 / 60 \mathrm{~Hz}, 50 \mathrm{VA}$ maximum. <br> EMC: Conforms to European Union Directive 89/336/EEC, EN61326-1. <br> SAFETY: Conforms to European Union Directive 73/23/ EEC, EN61010-1. <br> EMI/RFI: Meets VDE 0871B and FCC Class B. <br> ENVIRONMENT: <br> Operating: $0^{\circ}-50^{\circ} \mathrm{C},<80 \%$ relative humidity $\left(0^{\circ}-35^{\circ} \mathrm{C}\right)$. Storage: $-25^{\circ}$ to $+65^{\circ} \mathrm{C}$. <br> DIMENSIONS, WEIGHT: 89 mm high $\times 216 \mathrm{~mm}$ wide $\times$ 375 mm deep ( $3^{1 / 2}$ in $\times 8^{1 / 2}$ in $\times 14^{3} / 4 \mathrm{in}$ ). Net weight $3.4 \mathrm{~kg}(71 / 2 \mathrm{lbs})$. |

1st line:20-character alphanumeric.
2nd line:32-character alphanumeric.

8 -pin micro-DIN connector for digital I/O
-pin micro-DIN for Trigger Link

BNC for External Trigger
BNC for Channel Ready

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ENVIRONMENT:
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375 mm deep $\left(3 \frac{1}{2}\right.$ in $\left.\times 81 / 2 \mathrm{in} \times 143 / \mathrm{in}\right)$. Net weight $3.4 \mathrm{~kg}\left(7^{1 / 2} \mathrm{lbs}\right)$.


Model 7001 rear panel
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